Infleqtion

Quantum Computer Roadmap
Last Update: February 2024

	2024	**2026**	**2028**
	Error Detection	**Error Correction**	**Commercial Advantage**
Logical Performance	Dual-species crosstalk–free measurement	Logical circuit depth >1,000 Universal quantum gate set	Logical circuit depth >1 million
Logical Qubits	2	>10	>100
Software	Tooling (QCVV) for verifying fault-tolerant properties	Optimized compilation of fault-tolerant circuit	Exponential speed-up demonstration
Enabling Technologies	Optimized laser pulse modulation	Advanced photonic beam steering	Real-time atom reloading

Target numbers are estimates and not guarantees of future performance